

### **REMARKS/ARGUMENTS**

Pending claim 8 was rejected under 35 U.S.C. §112 ¶2. Claim 8 has been amended to overcome the rejection.

Pending claims 1 and 9 were rejected under 35 U.S.C. §102(e) over U.S. Patent Application No. 2002/0147932 (Brock). Applicant respectfully traverses the rejection.

With regard to amended claim 1, nowhere does Brock disclose an integrated circuit that includes a plurality of processor units and a controller to vary a clock frequency of the processor units. Instead, Brock teaches that processor clocks are controlled by a controller that is on a separate integrated circuit than the processors. In this regard, Brock teaches that a controller 201 separate from the processors is used to control clocks. Accordingly, the rejection of claim 1 is overcome and claims 1 and 9 are patentable over Brock.

Pending claims 2-8 and 19-20 were rejected under 35 U.S.C. §103(a) over Brock in view of U.S. Patent No. 5,790,877 (Nishiyama). Applicant respectfully traverses the rejection. Brock fails to teach or suggest on-chip controlling of clocks, while Nishiyama fails to teach or suggest control of more than a single processor. Furthermore, there is no motivation to combine the single processor of Nishiyama with the multiprocessor (e.g., server) system of Brock. Accordingly, pending claims 2-8 and 19-20 are patentable.

For at least these same reasons, dependent claims 21-23 that depend from claim 1 are patentable over the cited references.

New claims 24-26 are patentable, as the cited art nowhere teaches or suggests a single substrate that includes a plurality of processor units and a wireless transceiver.

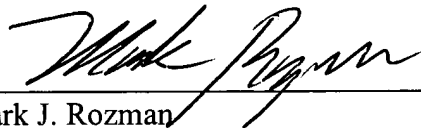
New claims 27-31 are patentable, as none of the cited references teach or suggest generating a plurality of clock signals on an integrated circuit and providing the clock signals to a plurality of processor units on the integrated circuit, where the clock signals are variable under control of the controller also on the integrated circuit.

The pending claims were also rejected under an obviousness-type double patenting rejection over co-pending Application No. 09/814,355. Enclosed herewith is a Terminal Disclaimer and accompanying fee. Accordingly, it is respectfully submitted that this rejection is overcome.

The application is believed to be in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

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